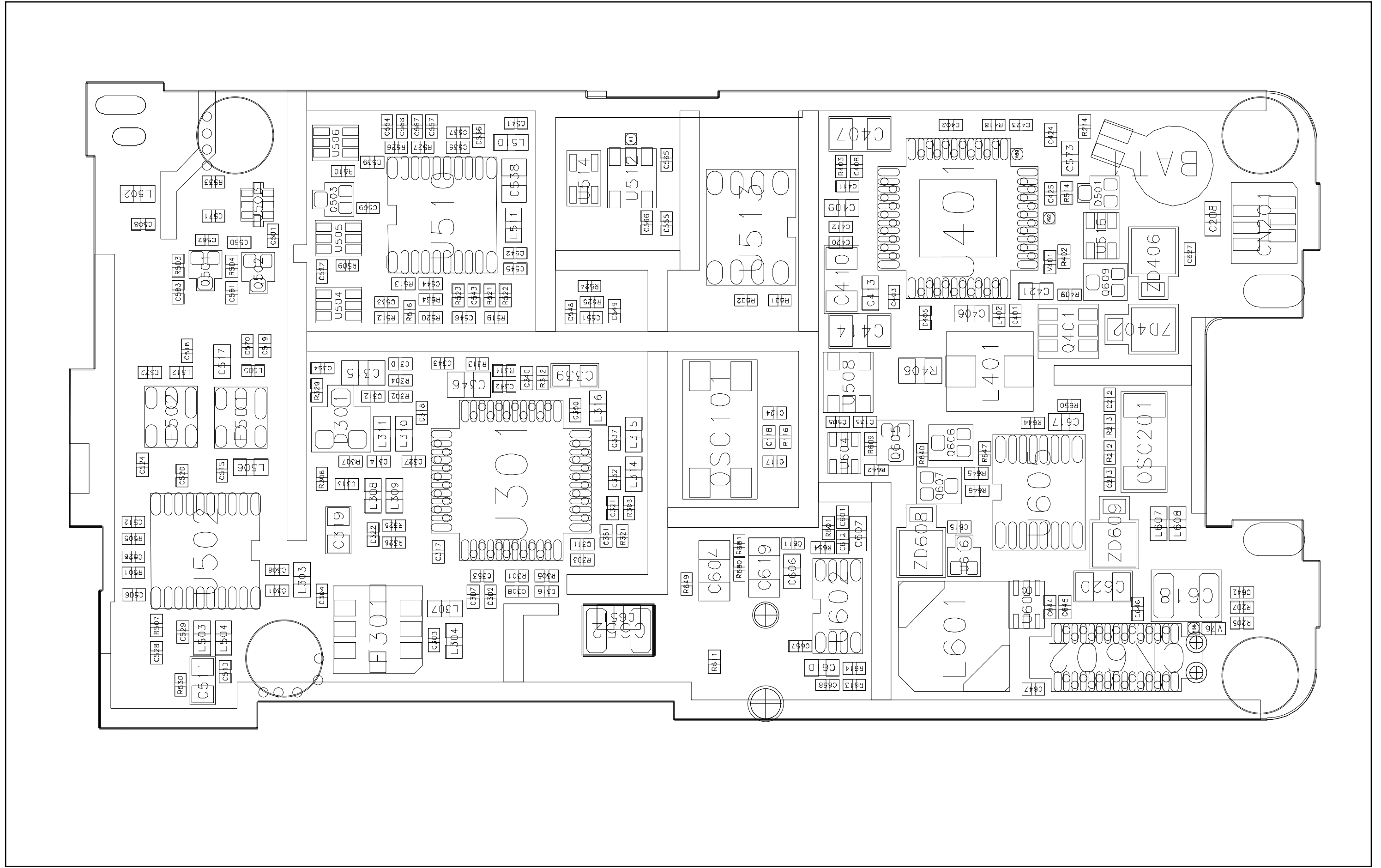
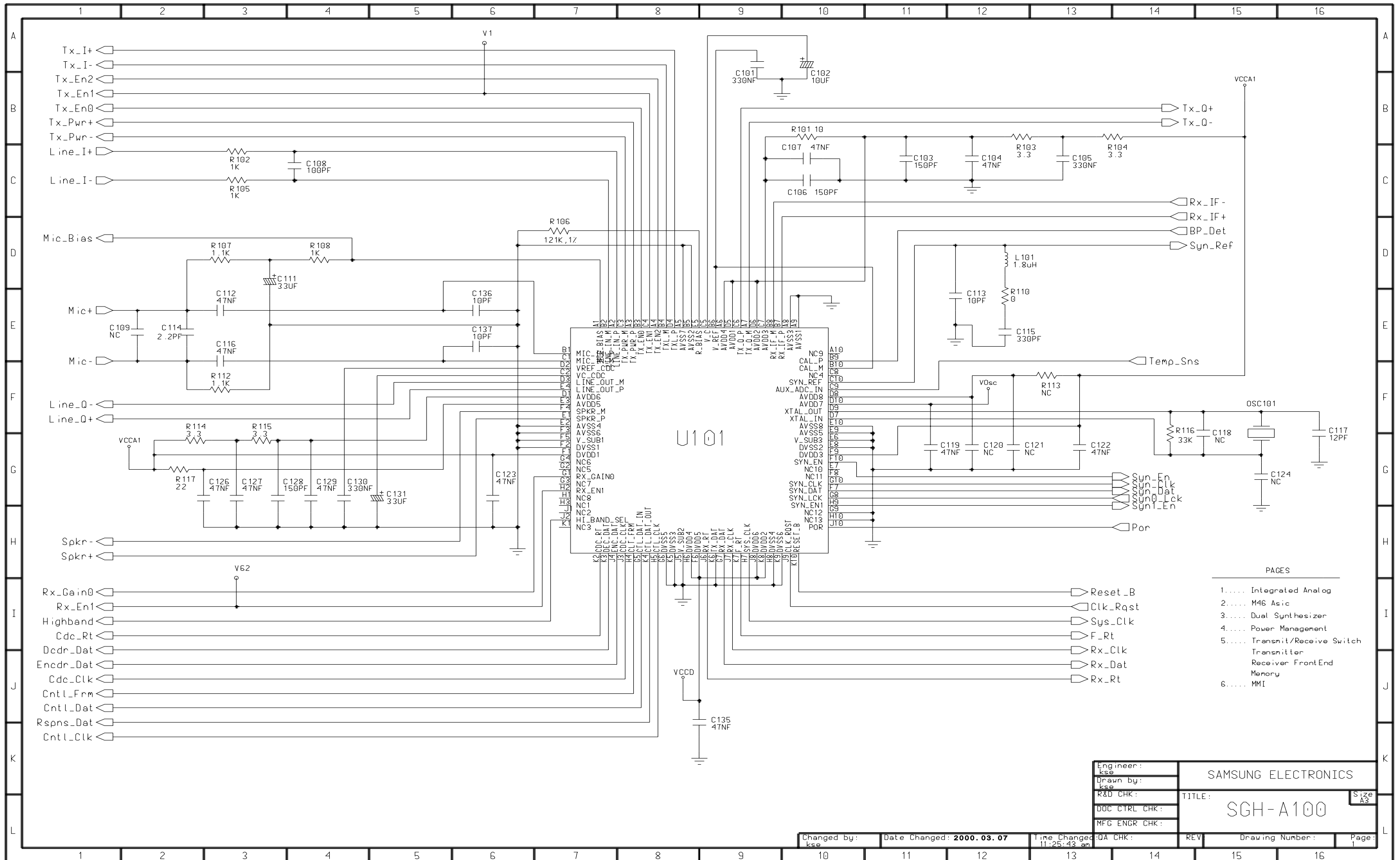


4-2 Main PCB Bottom Diagram

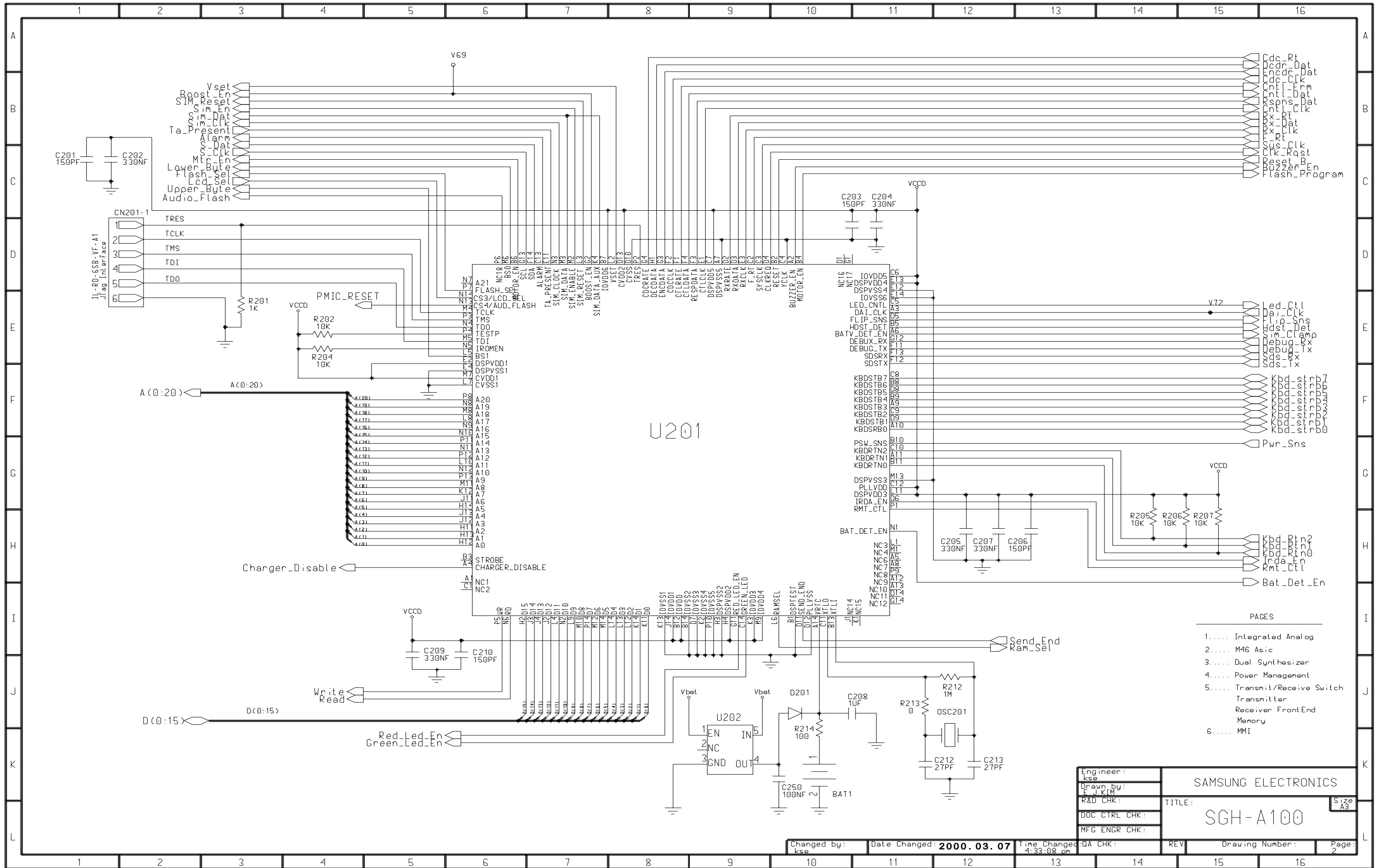


5. Circuit Diagrams

5-1 Integrated Analog Circuit Diagram



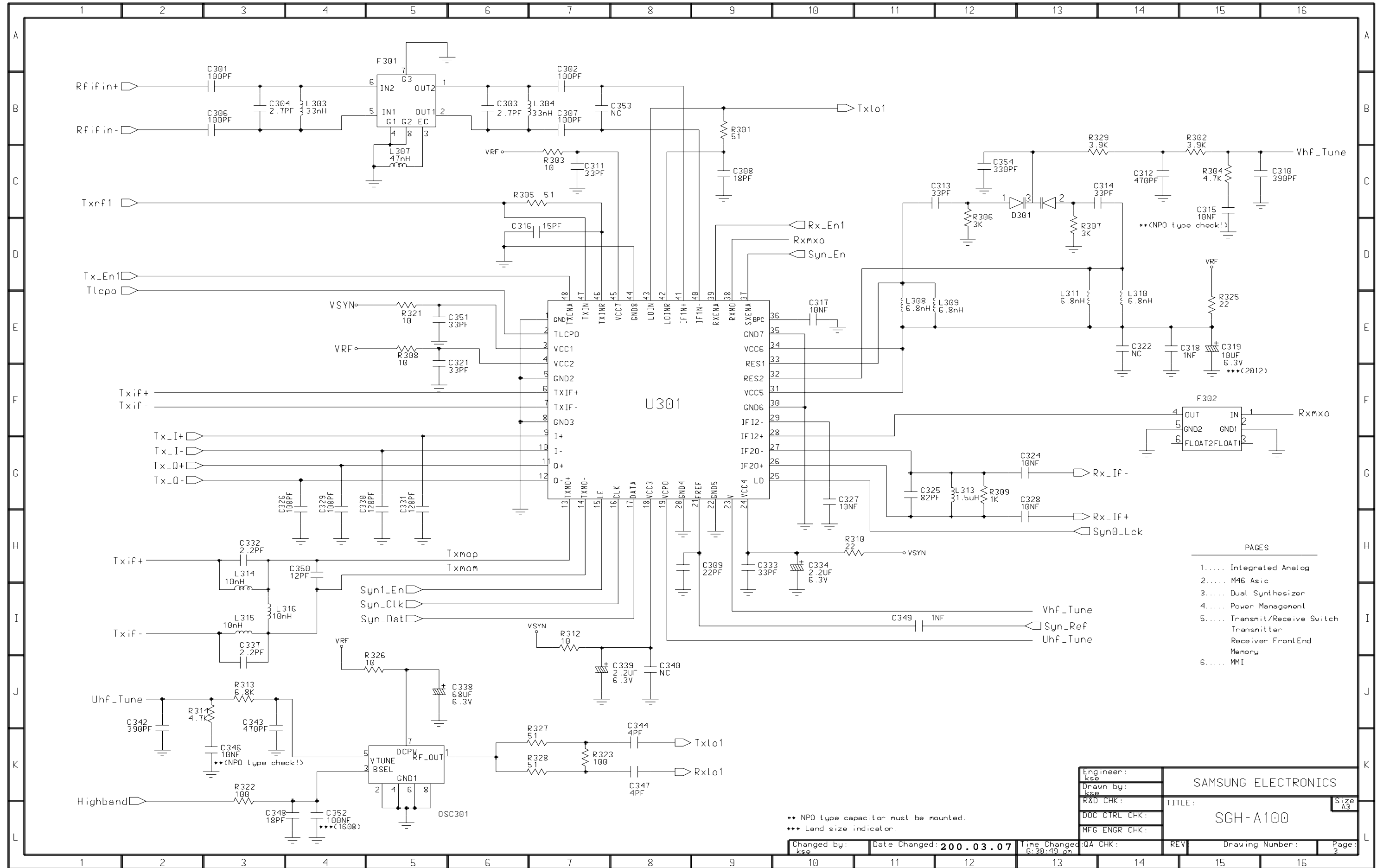
5-2 M46 Asic Circuit Diagram



Engineer:	SAMSUNG ELECTRONICS		
Drawn by:	F. J. KIM		
R&D CHK:	TITLE: SGH-A100		
DOC CTRL CHK:	Size A3		
MFG ENGR CHK:	Page 2		

Changed by:	Date Changed:	Time Changed:	QA CHK:	REV	Drawing Number:	Page:
ksj	2000.03.07	4:33:08 pm				2

5-3 Dual Synthesizer Circuit Diagram



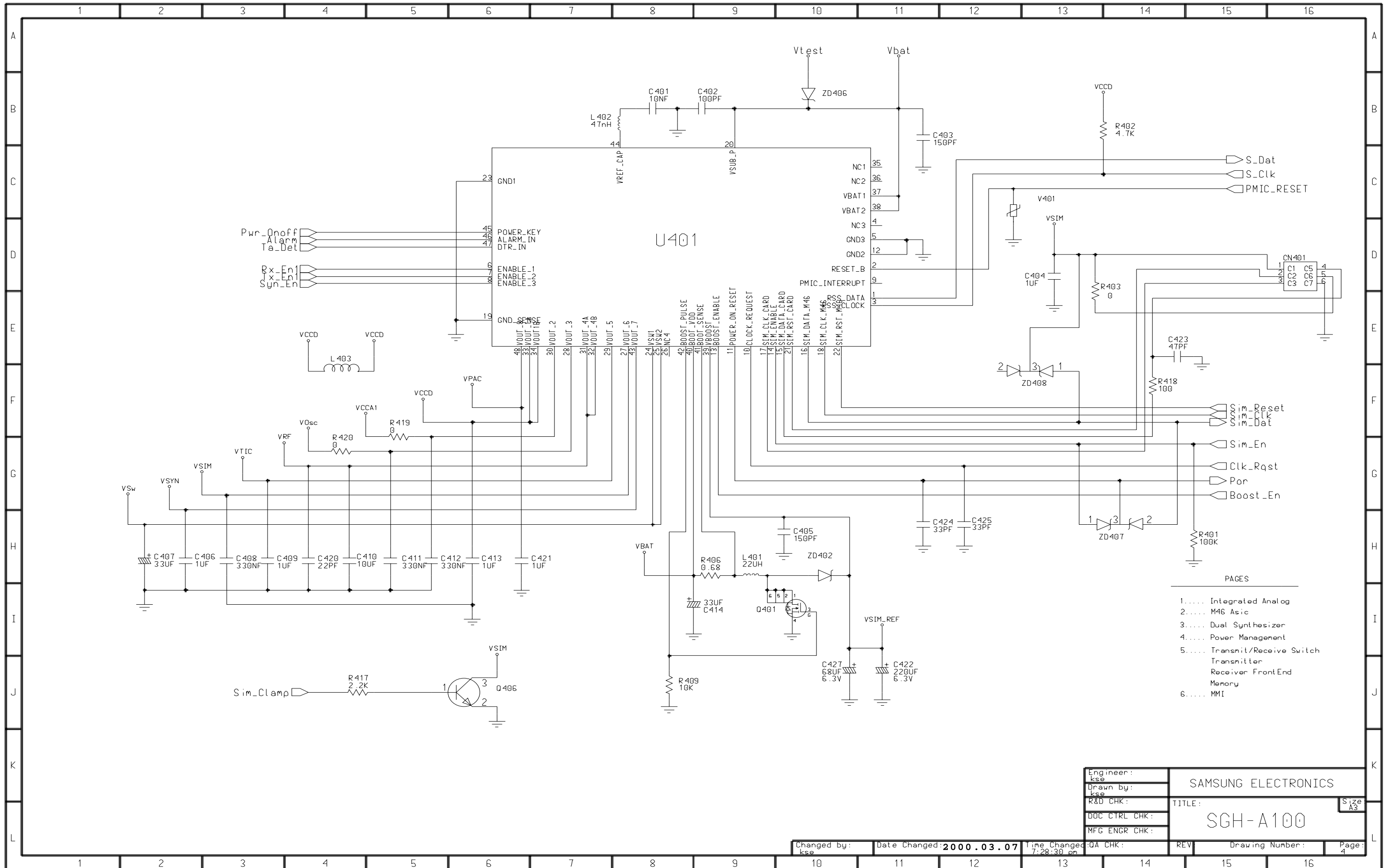
PAGES	
1.....	Integrated Analog
2.....	M46 Asic
3.....	Dual Synthesizer
4.....	Power Management
5.....	Transmit/Receive Switch Transmitter Receiver FrontEnd Memory
6.....	MMI

Engineer: kse	SAMSUNG ELECTRONICS
Drawn by: kse	
R&D CHK:	
DOC CTRL CHK:	
MFG ENGR CHK:	
TITLE: SGH-A100	

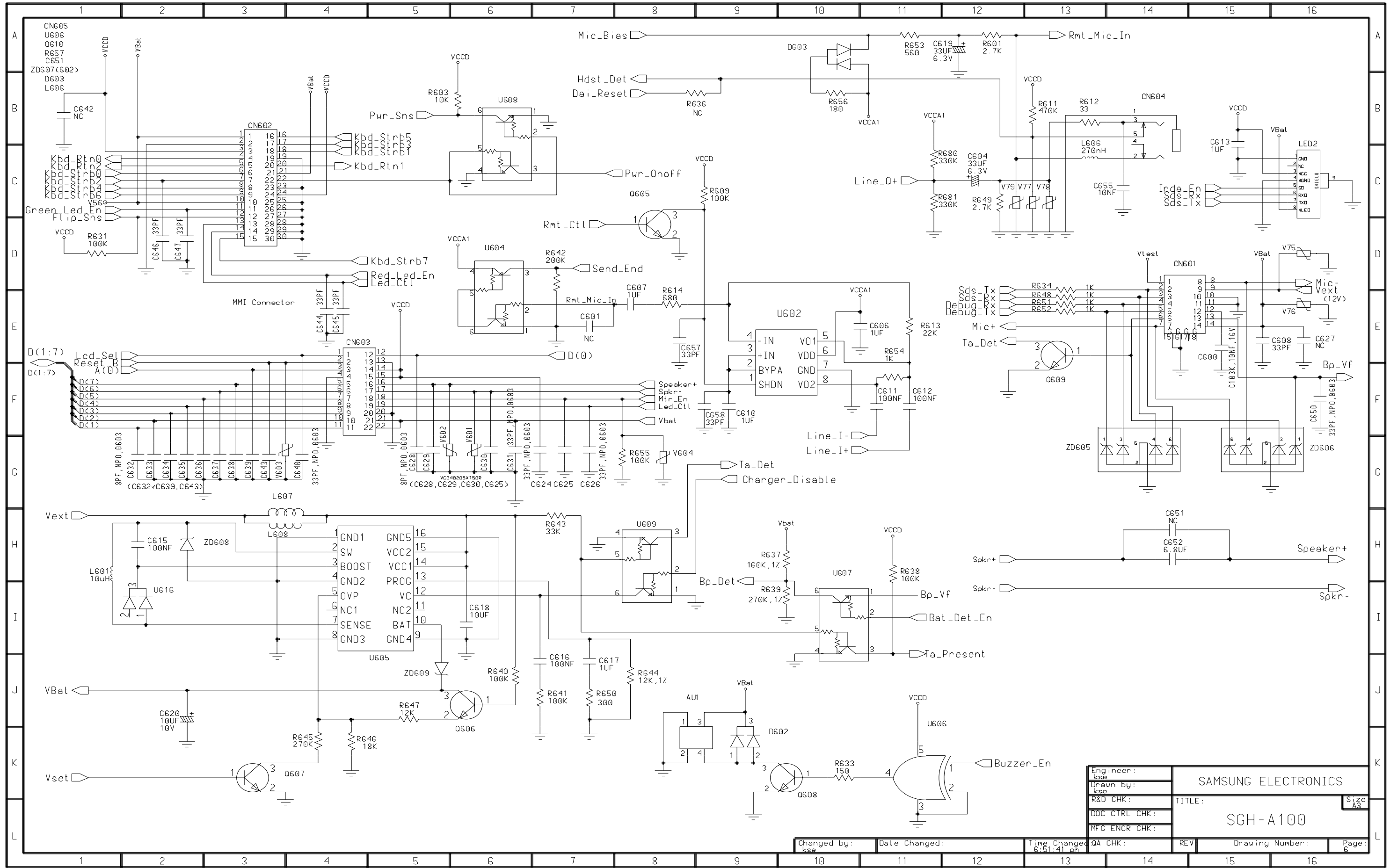
** NPO type capacitor must be mounted.
*** Land size indicator.

Changed by: kse	Date Changed: 200.03.07	Time Changed: 6:30:49 pm	QA CHK:	REV:	Drawing Number:	Page: 3
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5-4 Power Management Circuit Diagram



5-6 MMI Circuit Diagram



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kse		6:51:41 pm				6