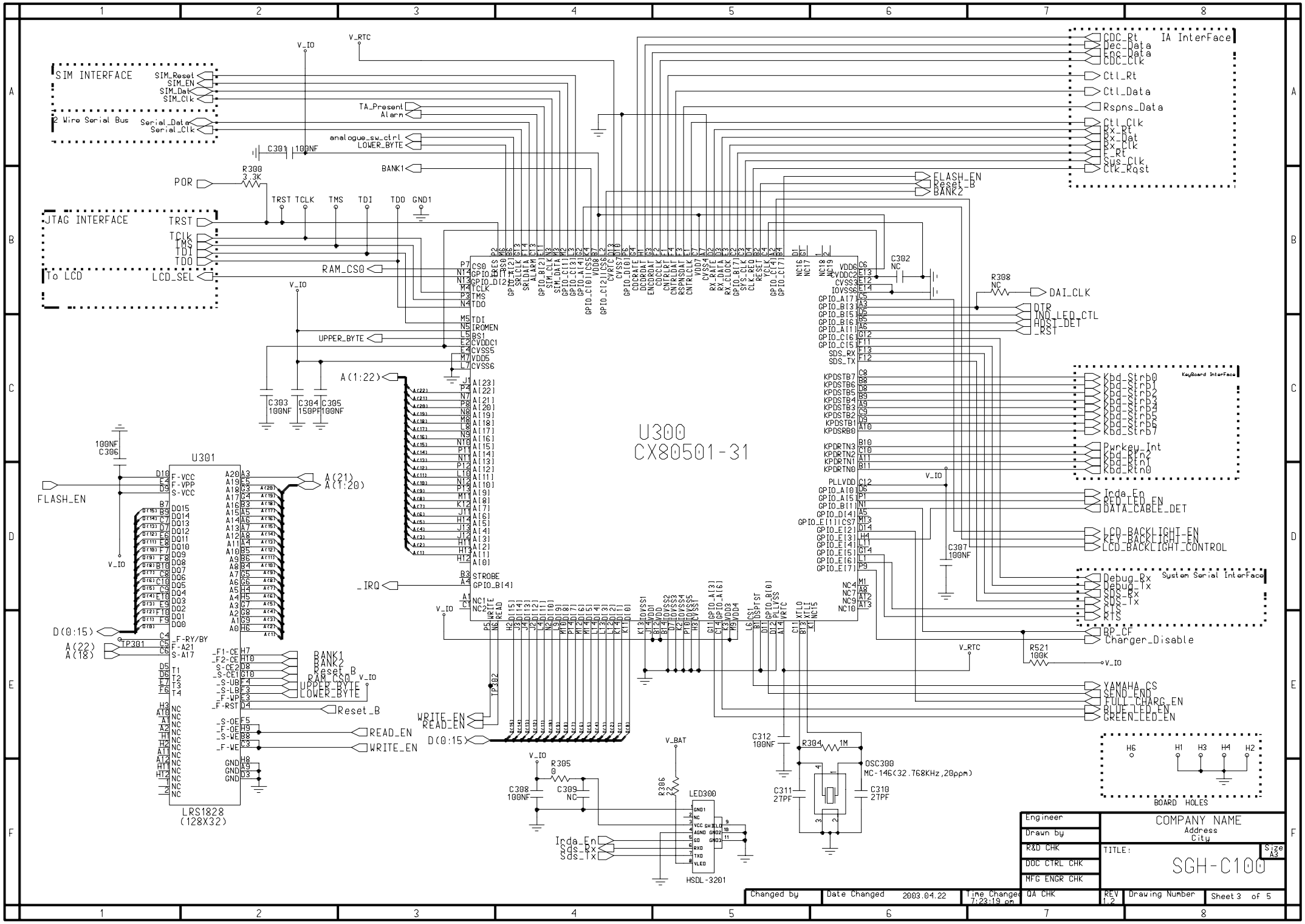
SIM INTERFACE
SIM_Reset
SIM_EN
SIM_Dat
SIM_Clk
2 Wire Serial Bus
Serial_Data
Serial_Clk
TA_Present
Alarm
analogue_sw_ctrl
LOWER_BYTE
BANK1
POR
R300
3.3K
C301
100NF
V_IO
V_RTC
JTAG INTERFACE
TRST
TCLK
TMS
TDI
TDO
To LCD
LCD_SEL
RAM_CS0
UPPER_BYTE
GND1
C303
100NF
C304
15PF
C305
100NF
A(1:22)
U300
CX80501-31
IA InterFace
CDC_Rt
Dec_Data
Enc_Data
CDC_Clk
Ctl_Rt
Ctl_Data
Rspns_Data
Ctl_Clk
Rx_Rt
Rx_Dat
Rx_Clk
F_Rt
Sys_Clk
Clk_Rqst
FLASH_EN
Reset_B
BANK2
C302
NC
R308
NC
DAI_CLK
DTR
IND_LED_CTL
HDSI_DET
_RST
KeyBoard InterFace
Kbd_Strb0
Kbd_Strb1
Kbd_Strb2
Kbd_Strb3
Kbd_Strb4
Kbd_Strb5
Kbd_Strb6
Kbd_Strb7
Pwrkey_Int
Kbd_Rtn2
Kbd_Rtn1
Kbd_Rtn0
Irda_En
RED_LED_EN
DATA_CABLE_DET
LCD_BACKLIGHT_EN
KEY_BACKLIGHT_EN
LCD_BACKLIGHT_CONTROL
C307
100NF
System Serial Interface
Debug_Rx
Debug_Tx
SDS_Rx
SDS_Tx
RTS
CTS
BP_CF
Charger_Disable
R521
100K
YAMAHA_CS
SEND_END
FUL_CHARG_EN
BLUE_LED_EN
GREEN_LED_EN
U301
LRS1828
(128X32)
100NF
C306
FLASH_EN
D(0:15)
A(22)
A(18)
TP301
A(21)
A(1:20)
_IRQ
WRITE_EN
READ_EN
TP302
Reset_B
C312
100NF
R304
1M
OSC300
MC-146(32.768KHz,20ppm)
C311
27PF
C310
27PF
V_BAT
R305
0
C308
100NF
C309
NC
R306
LED300
HSDL-3201
Irda_En
Sds_Rx
Sds_Tx
BOARD HOLES
H6
H1
H3
H4
H2
Engineer
Drawn by
R&D CHK
DOC CTRL CHK
MFG ENGR CHK
COMPANY NAME
Address
City
TITLE:
SGH-C100
Size
A3
Changed by
Date Changed
2003.04.22
Time Changed
7:23:19 pm
QA CHK
REV
1.2
Drawing Number
Sheet 3 of 5