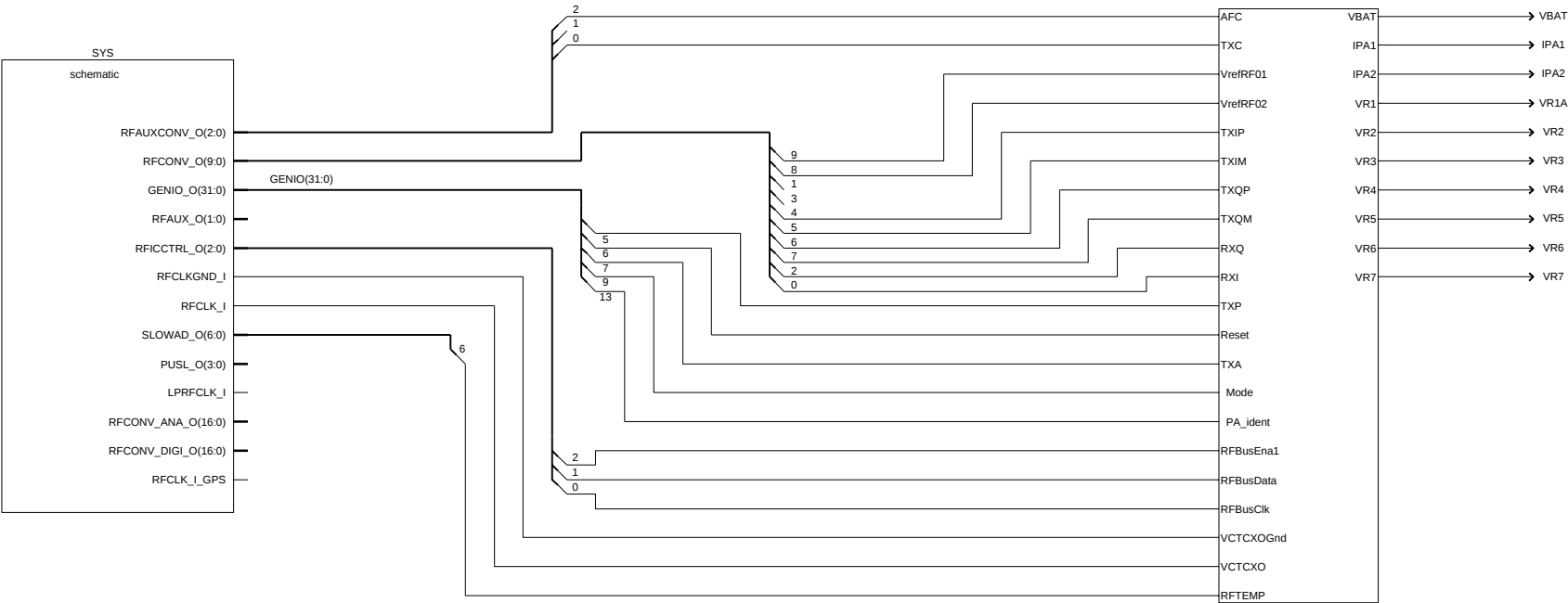
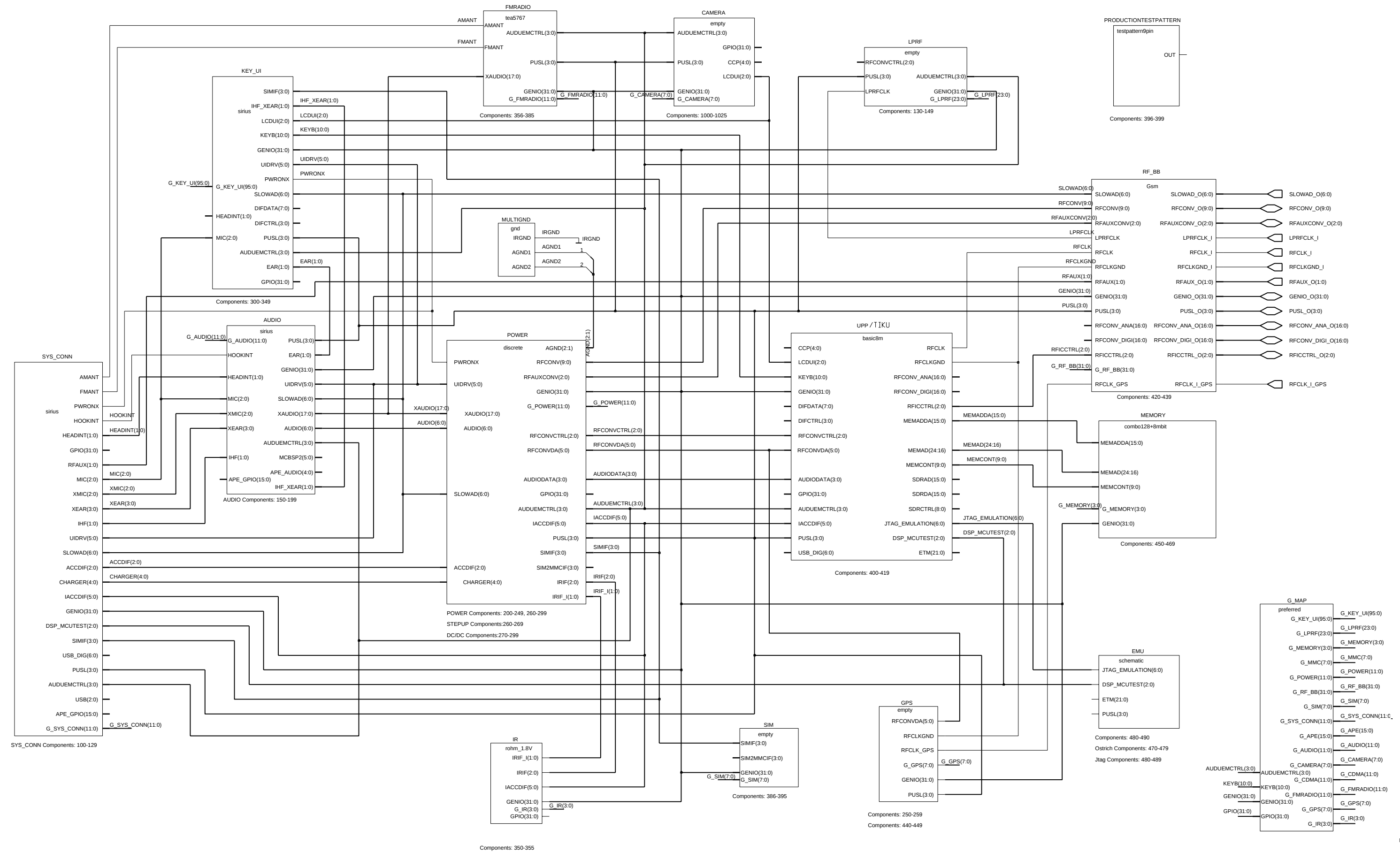


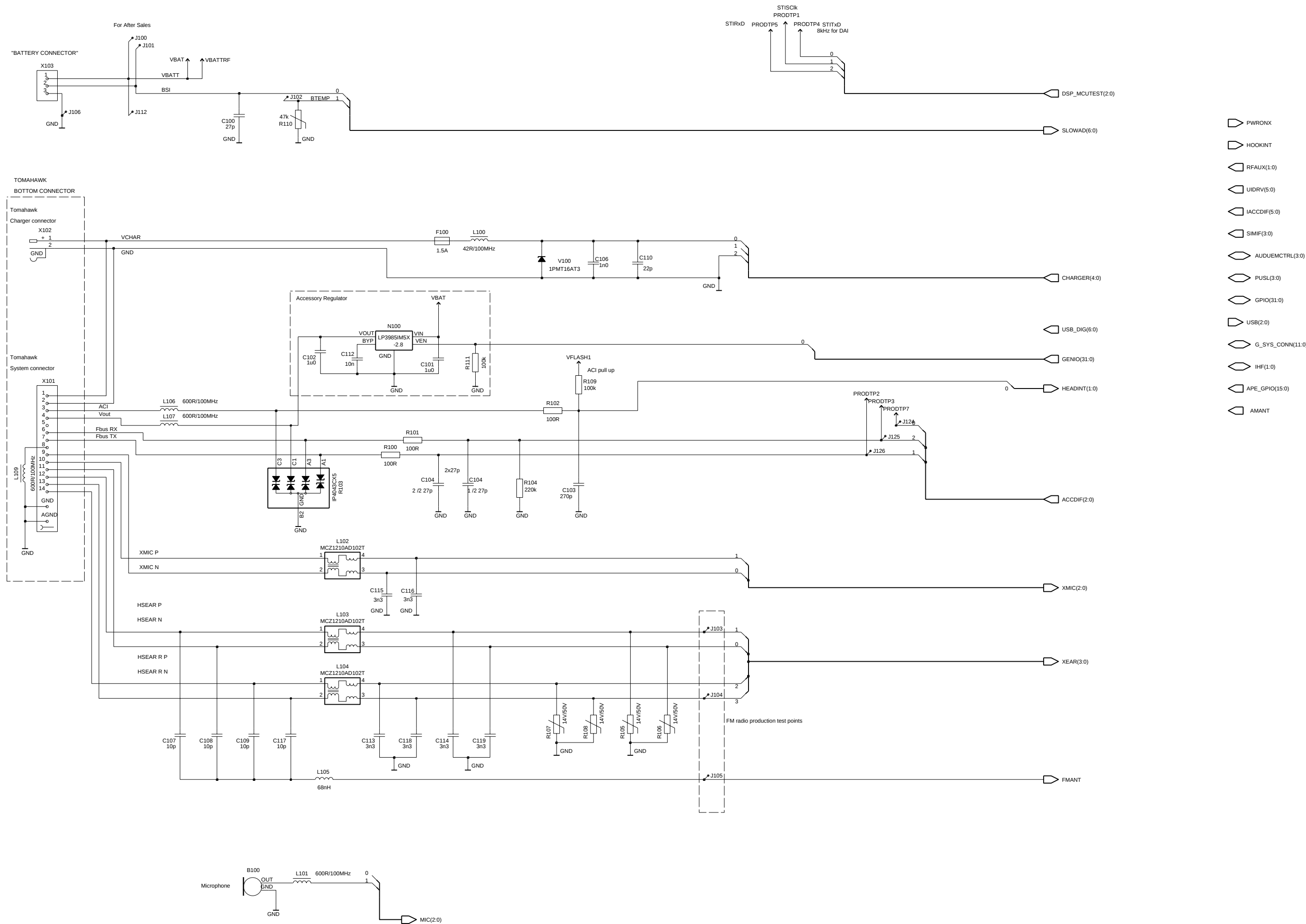
Top Level



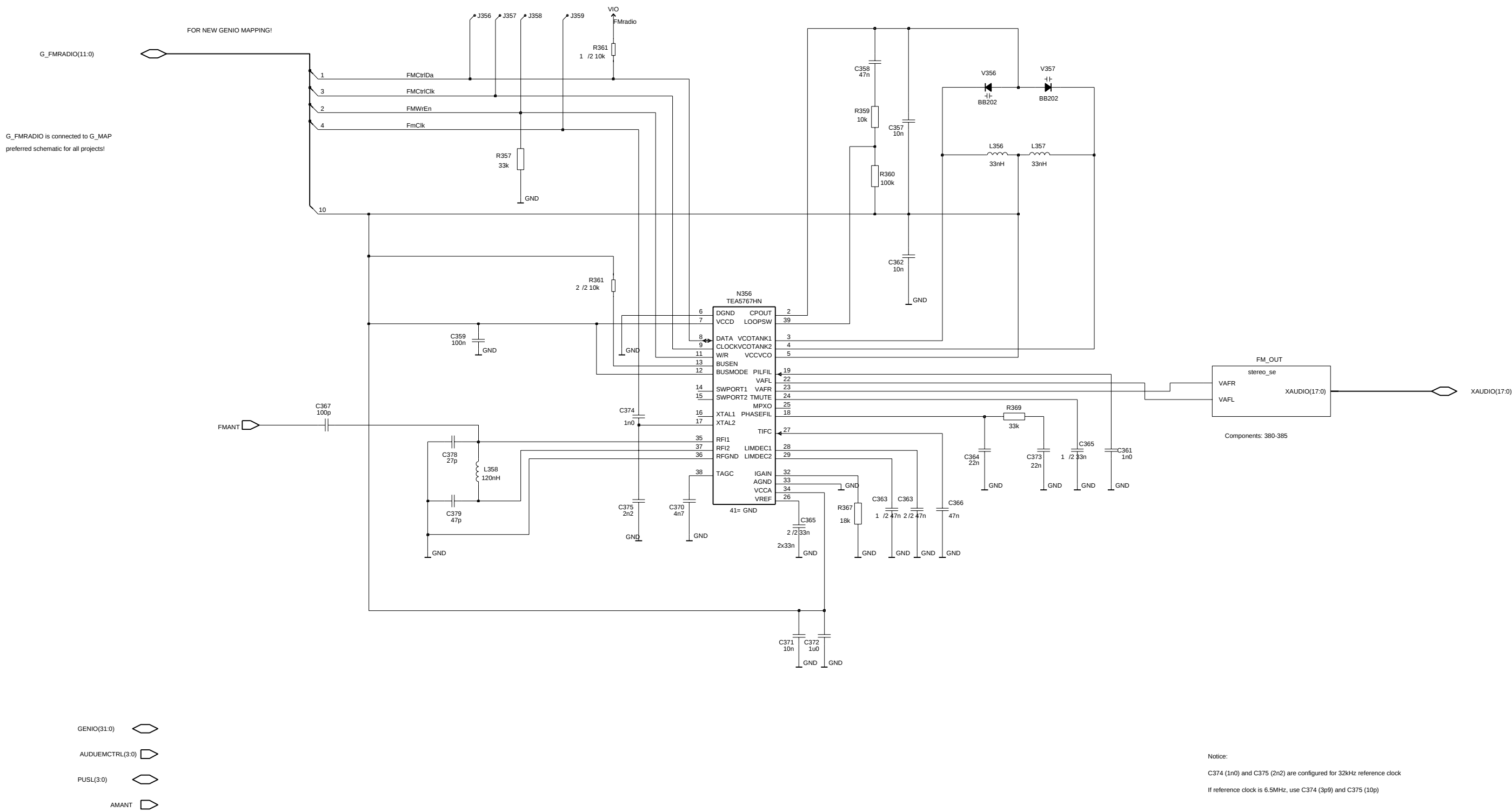
Common BB



System Connector



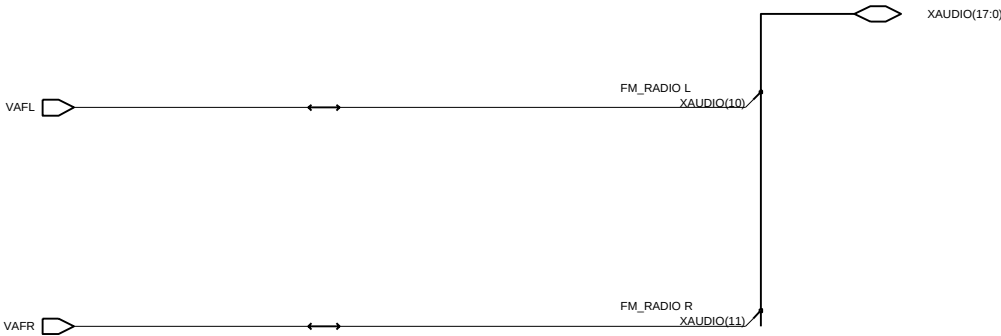
FM Radio



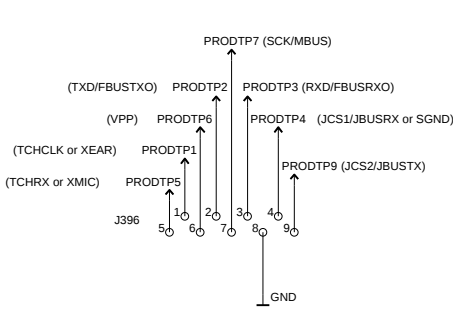
Notice:

C374 (1n0) and C375 (2n2) are configured for 32kHz reference clock

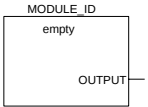
If reference clock is 6.5MHz, use C374 (3p9) and C375 (10p)



9 Pin Production Test Pattern

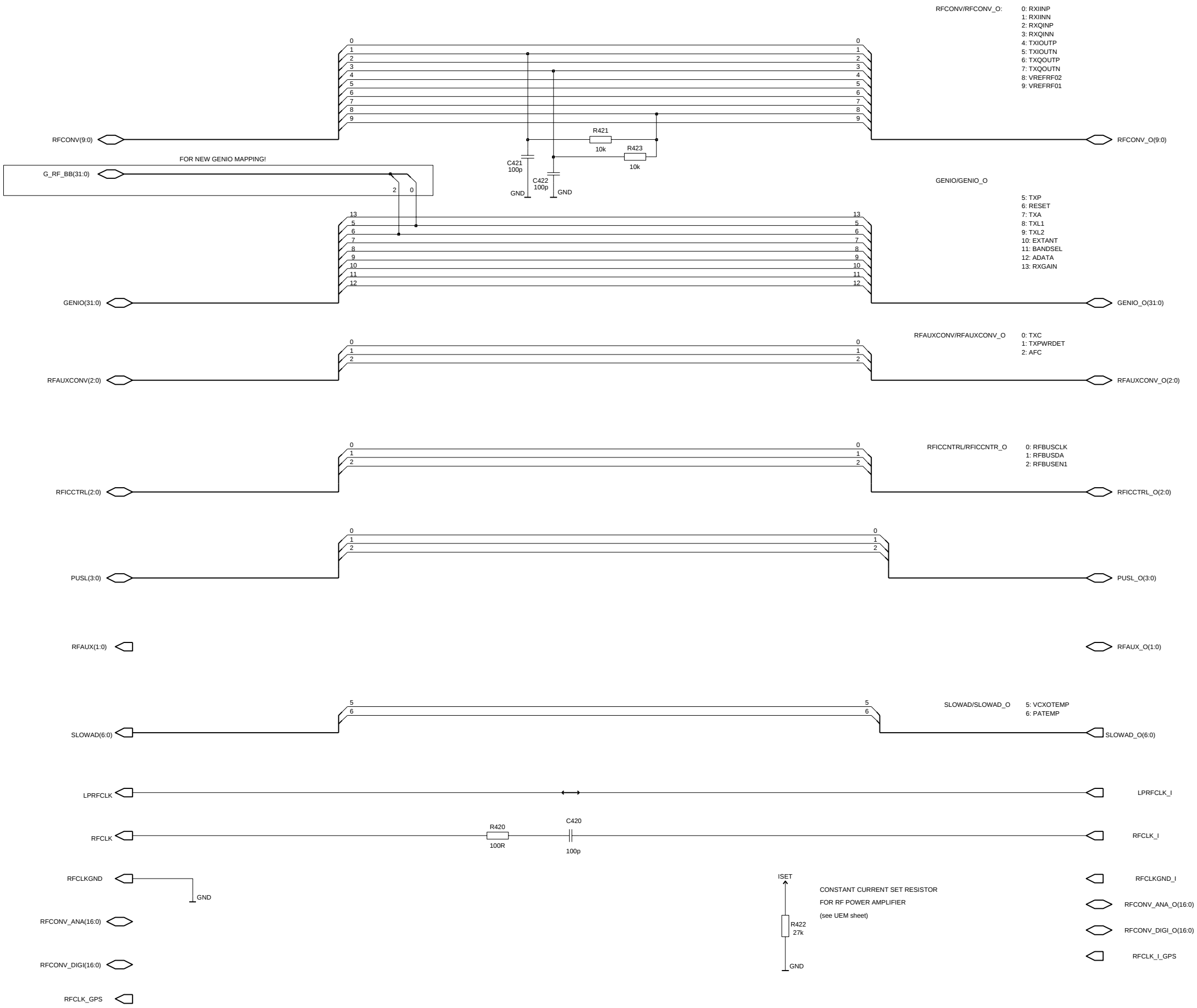


OUT

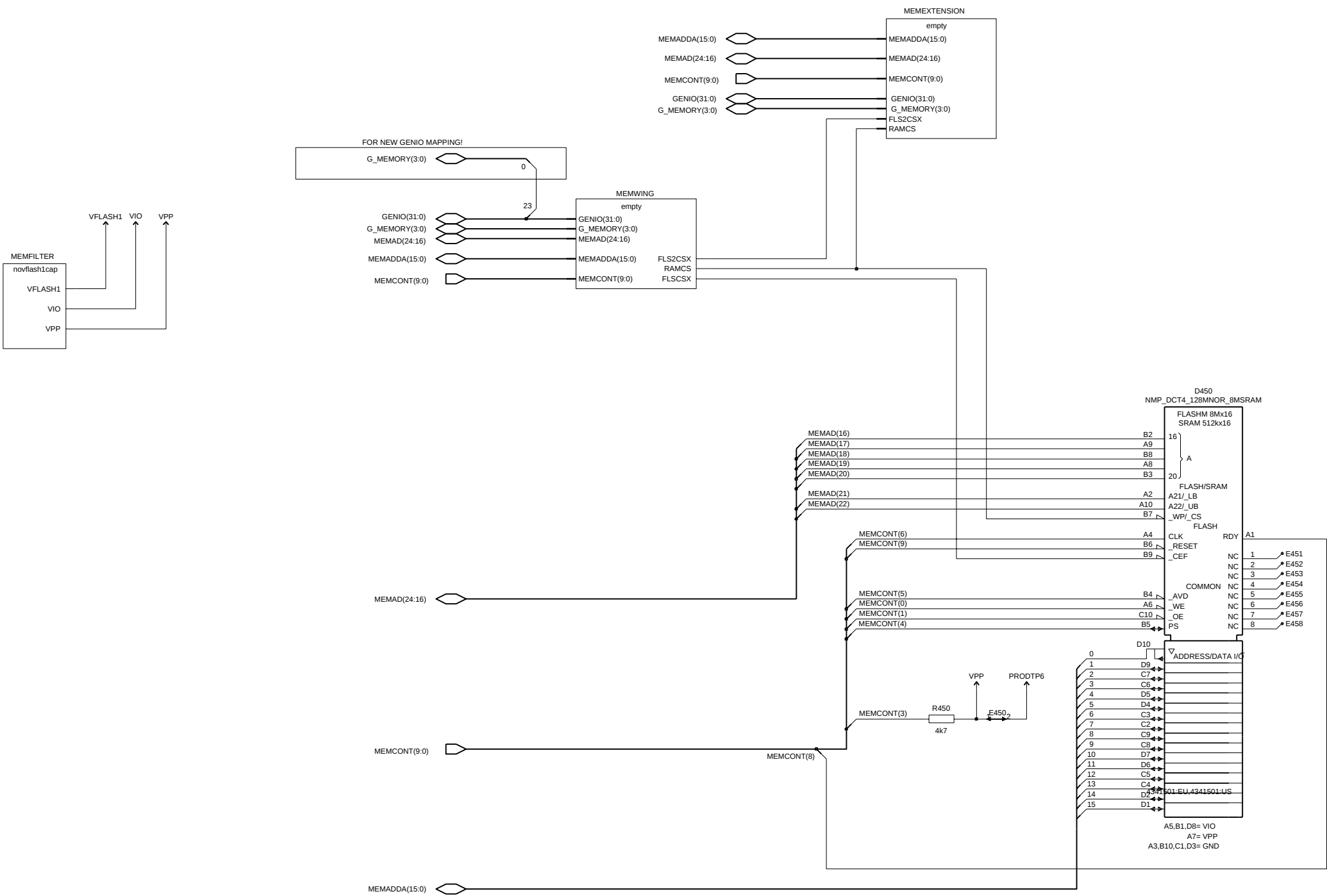


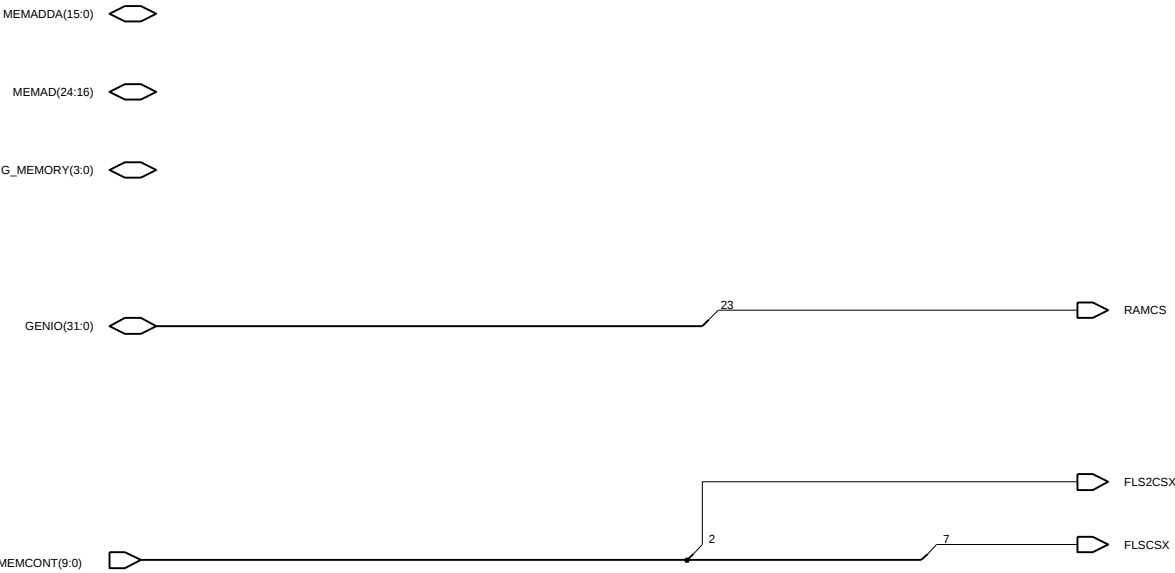
Module_id: 399

GSM RF - BB Interface

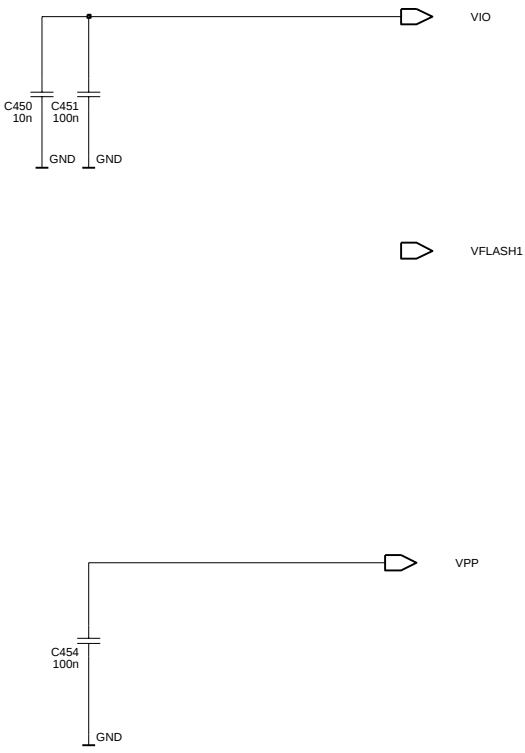


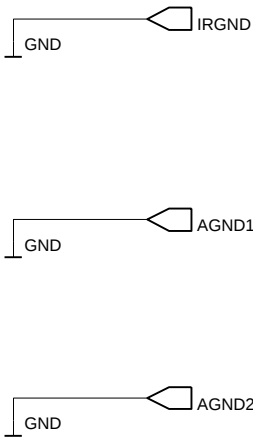
Combo Memory



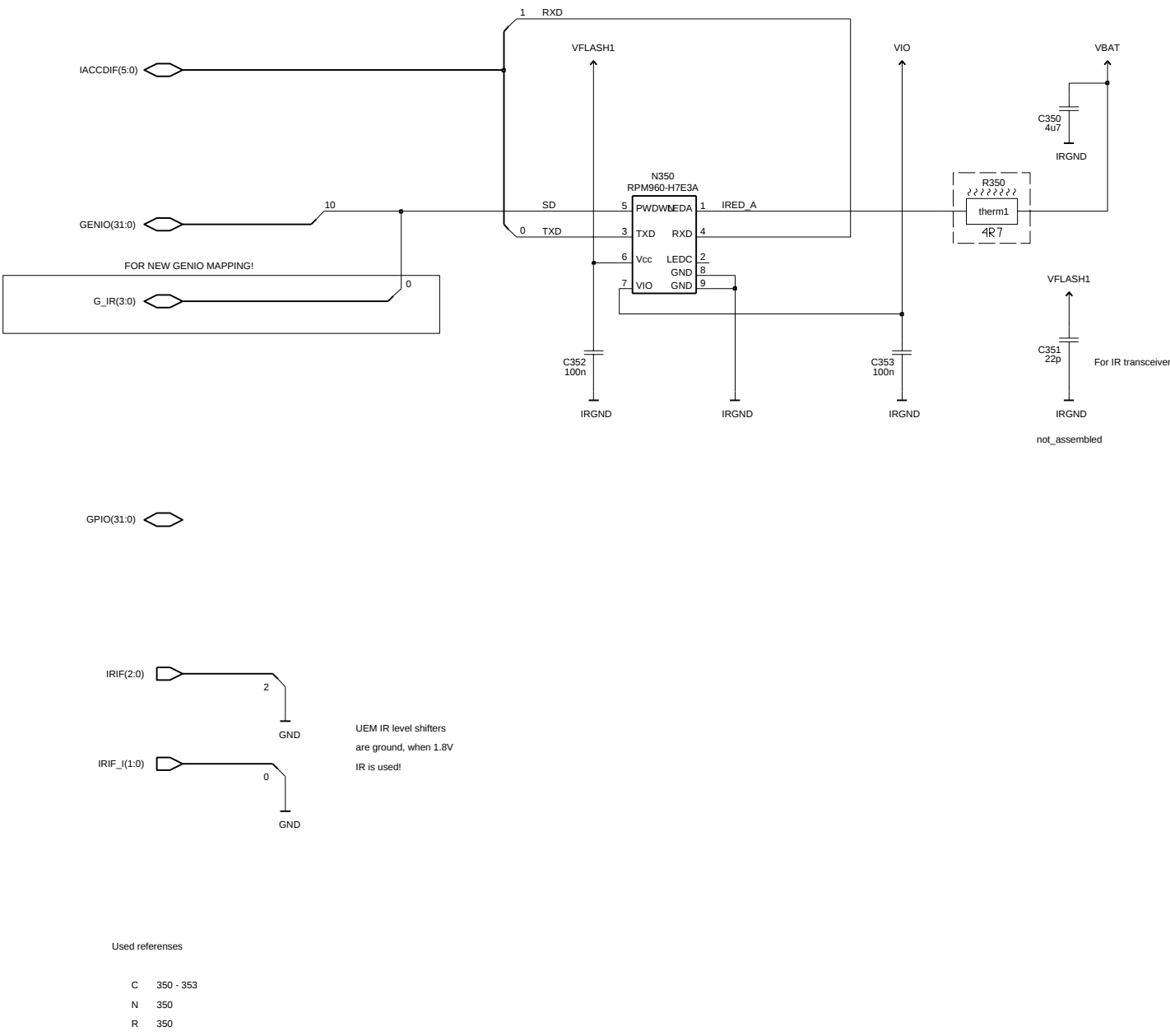


Discrete Capacitors for Memory Without VFlash1



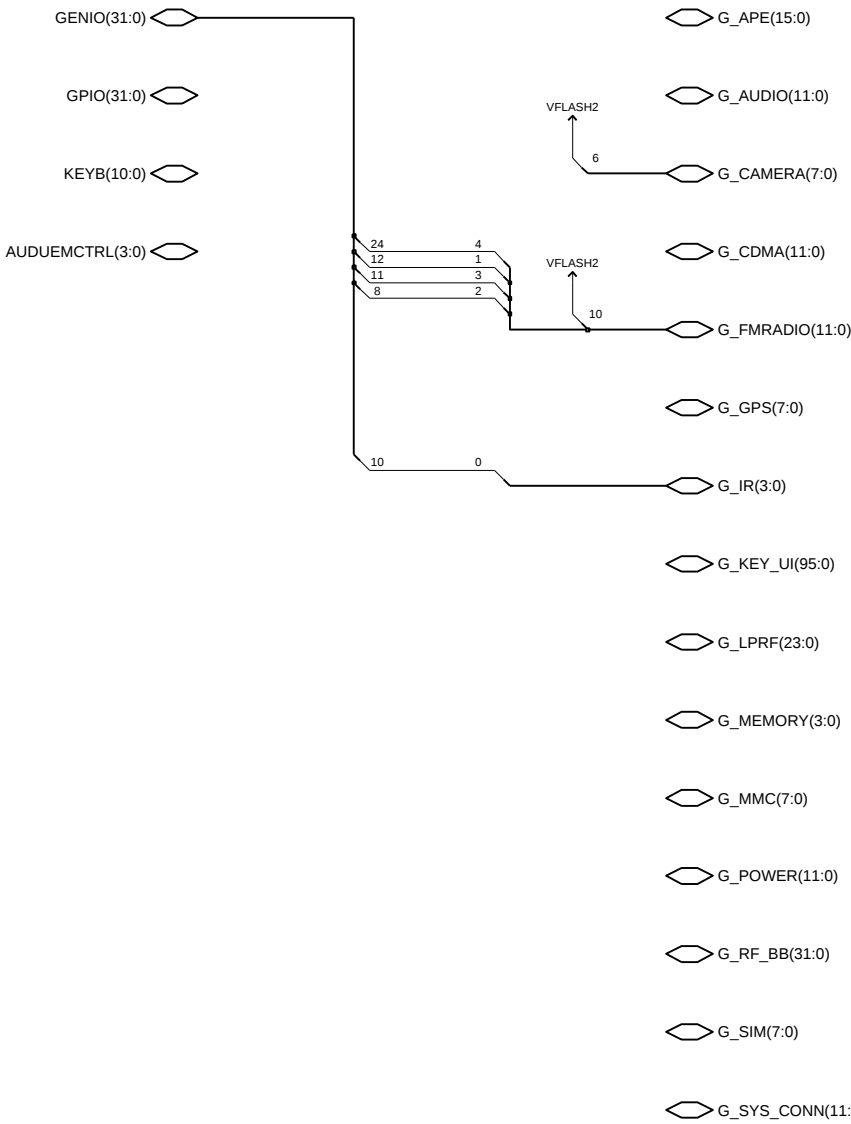


IR Module



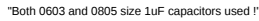
GENIO and GPIO Connection Block

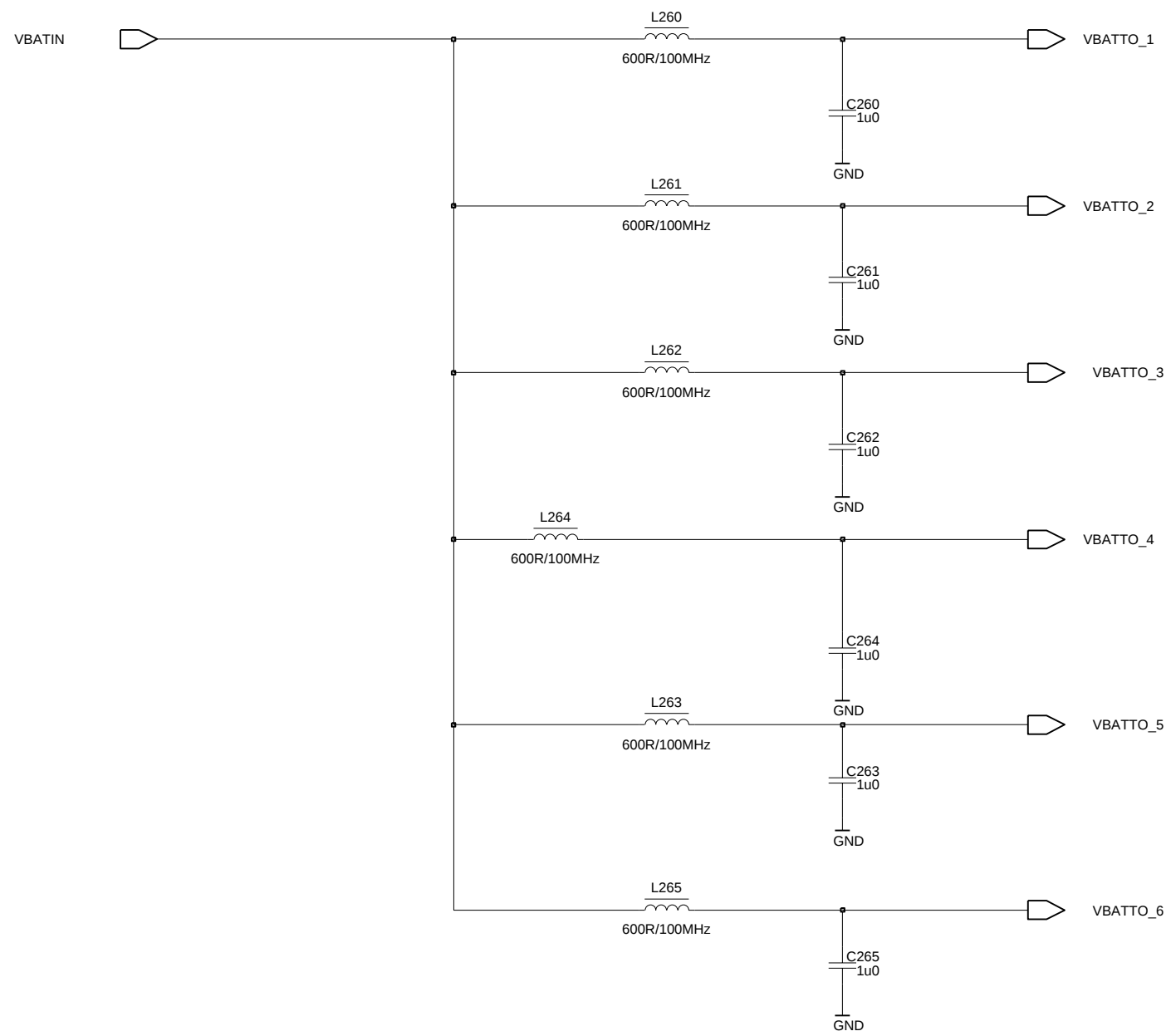
That connections used only if TEA5767 or CIT+SHARP_1.8V schematics are selected

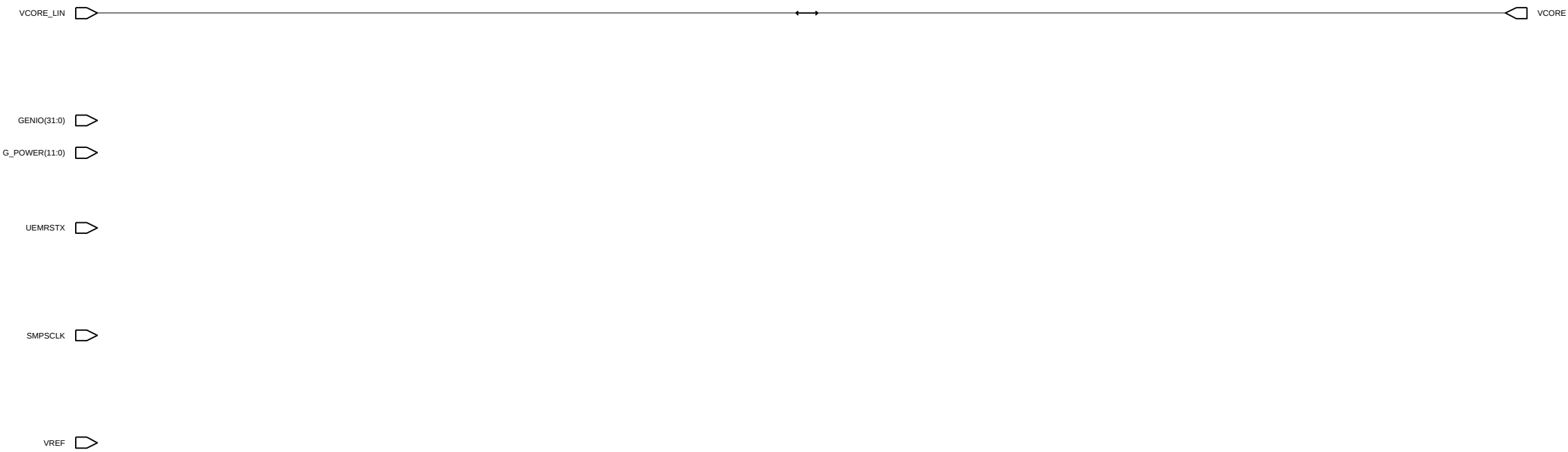


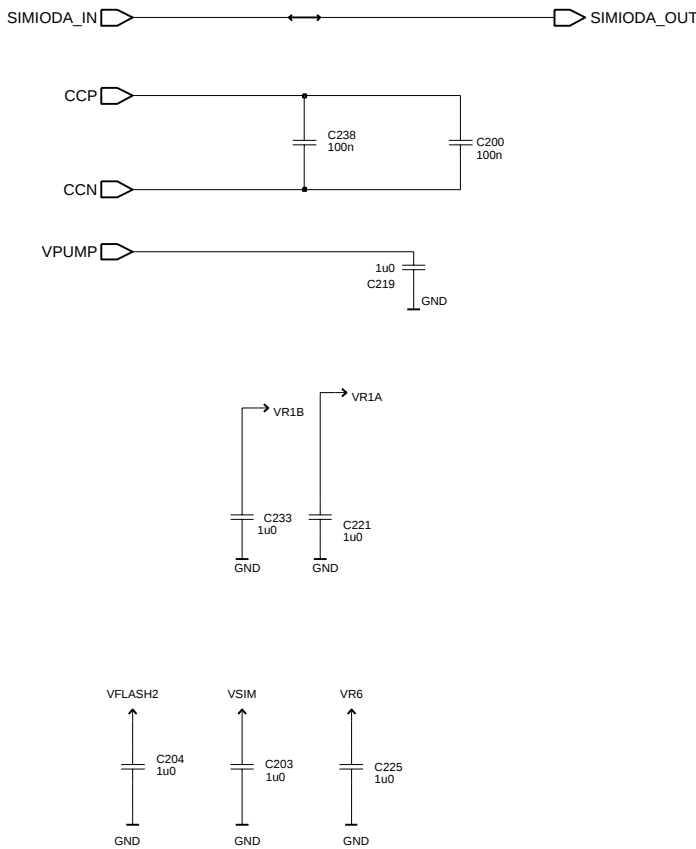


Discrete Power Management

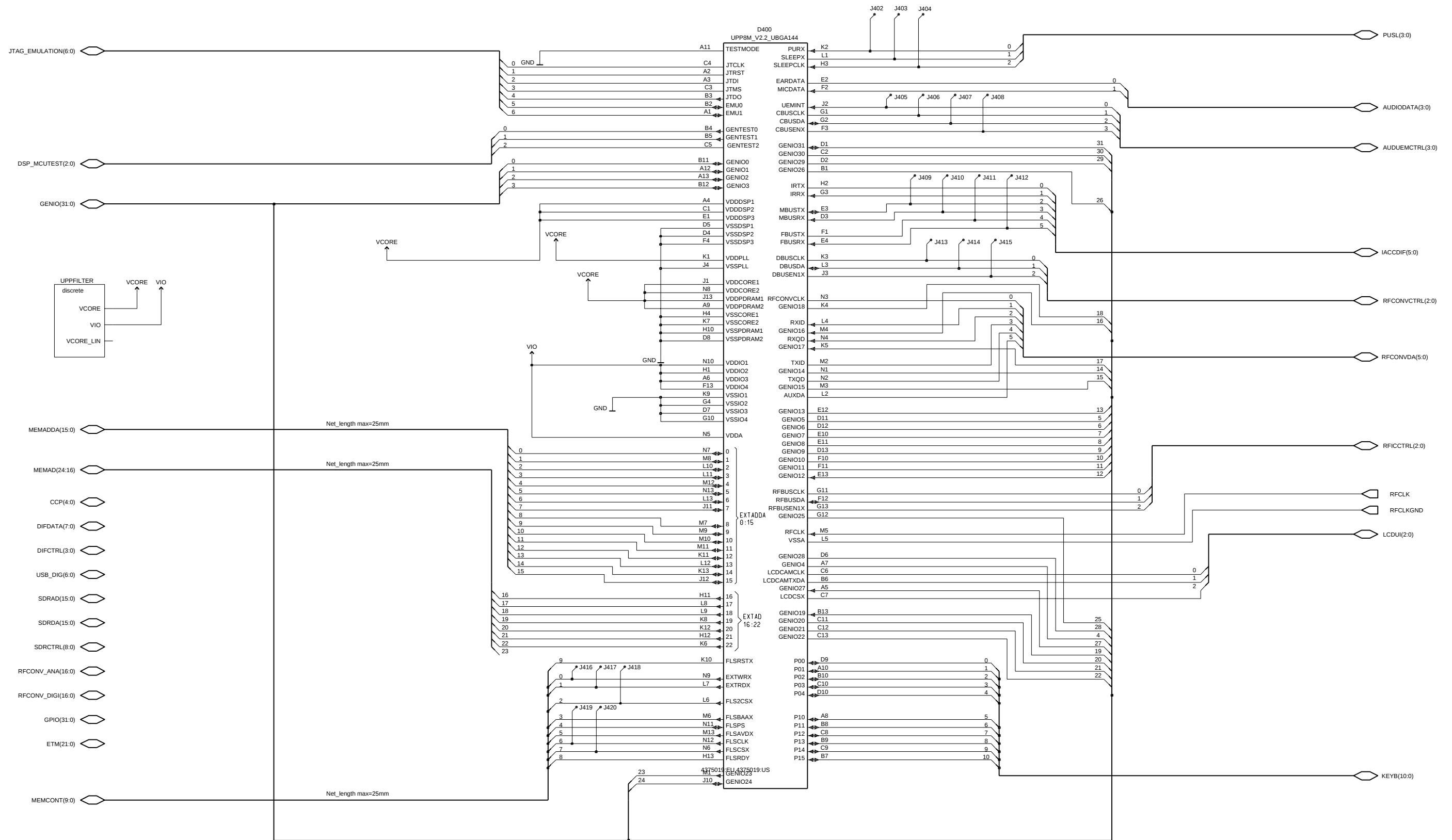


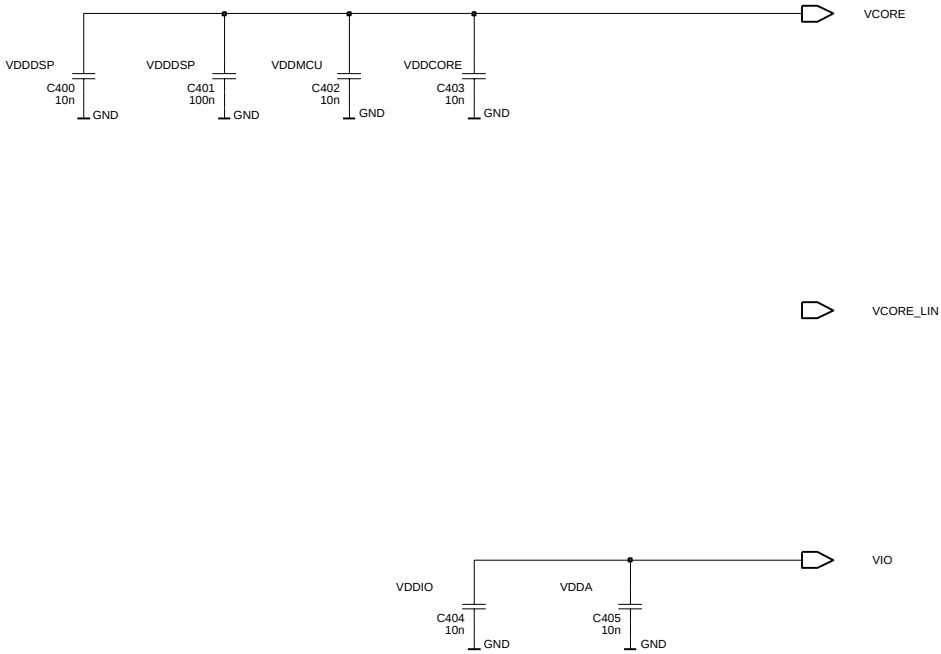






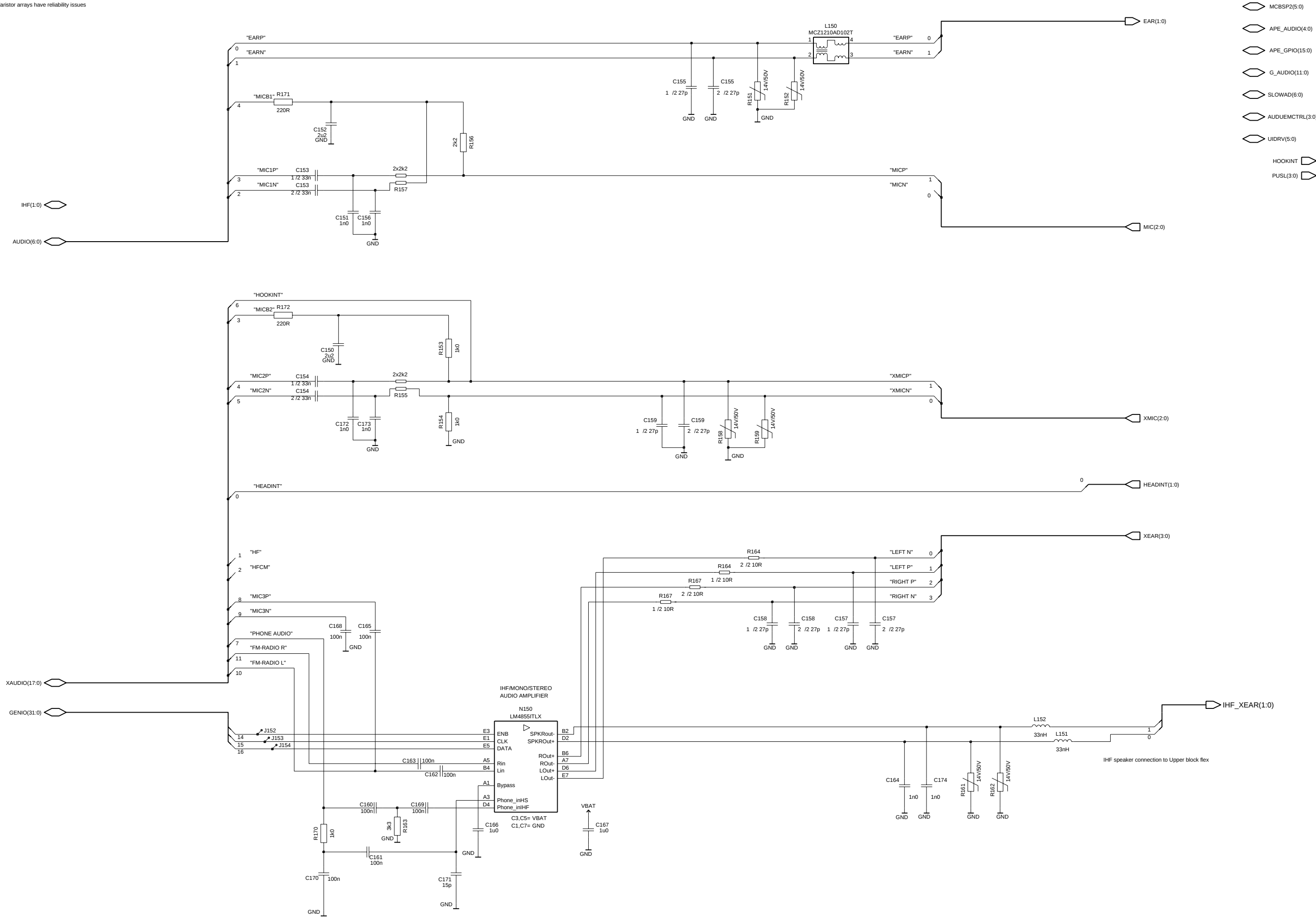
UPP 8M Implementation



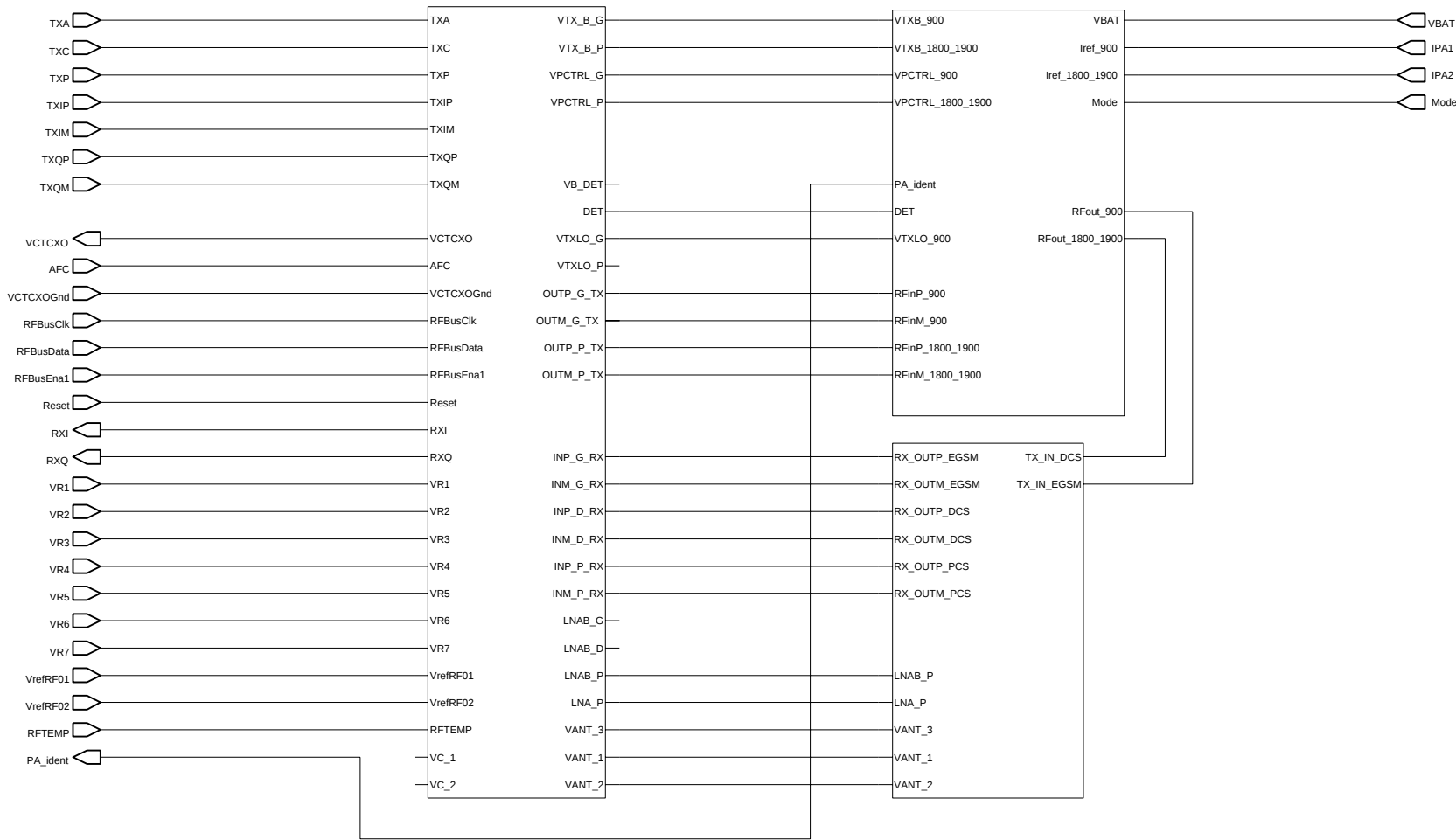


Audio

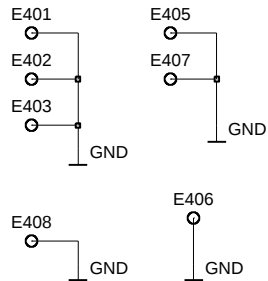
Varistor arrays have reliability issues



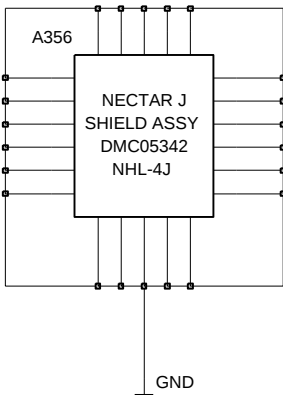
RF Top Level



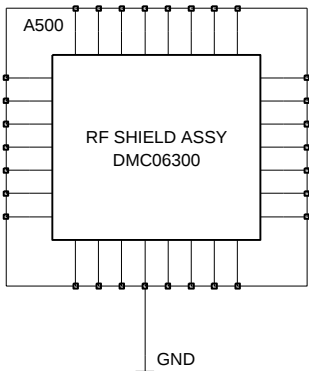
Mechanical plated through holes



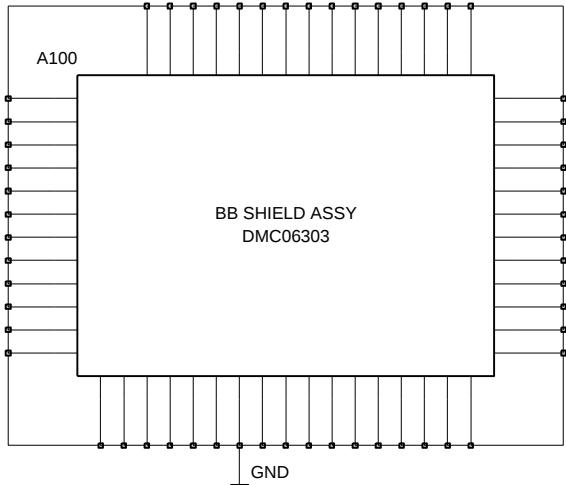
Radio shield



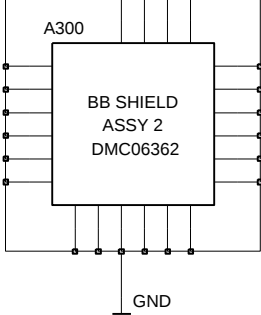
RF1 shield (PA)



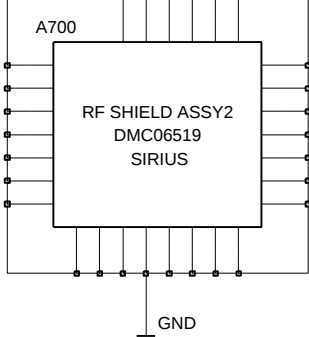
BB shield



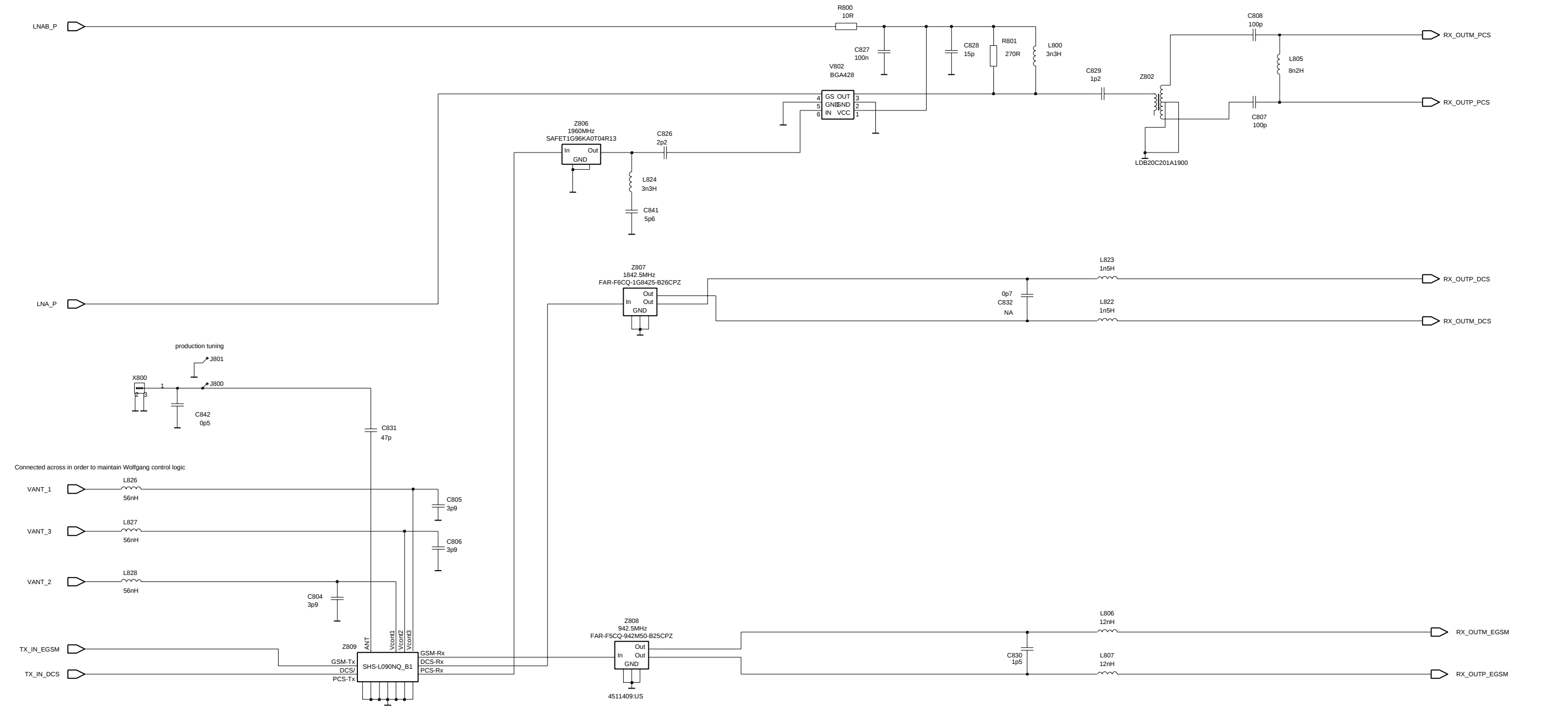
HW accelerator shield



RF2 shield (Helgo)



RX Front End and Antenna Switch



Last references:

C842

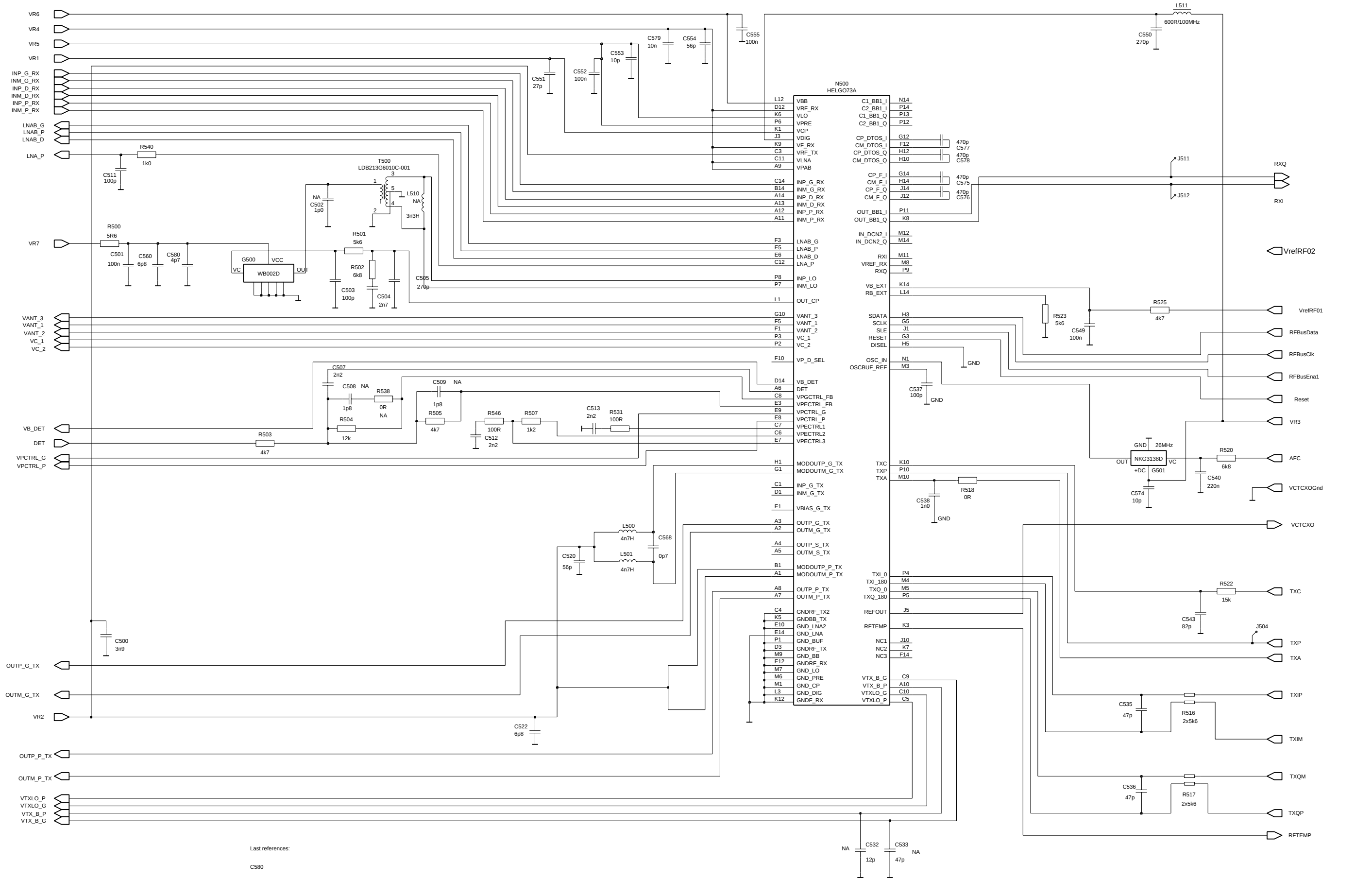
R807

L830

Z809

V801

HELGA



Last references:

C580

R550

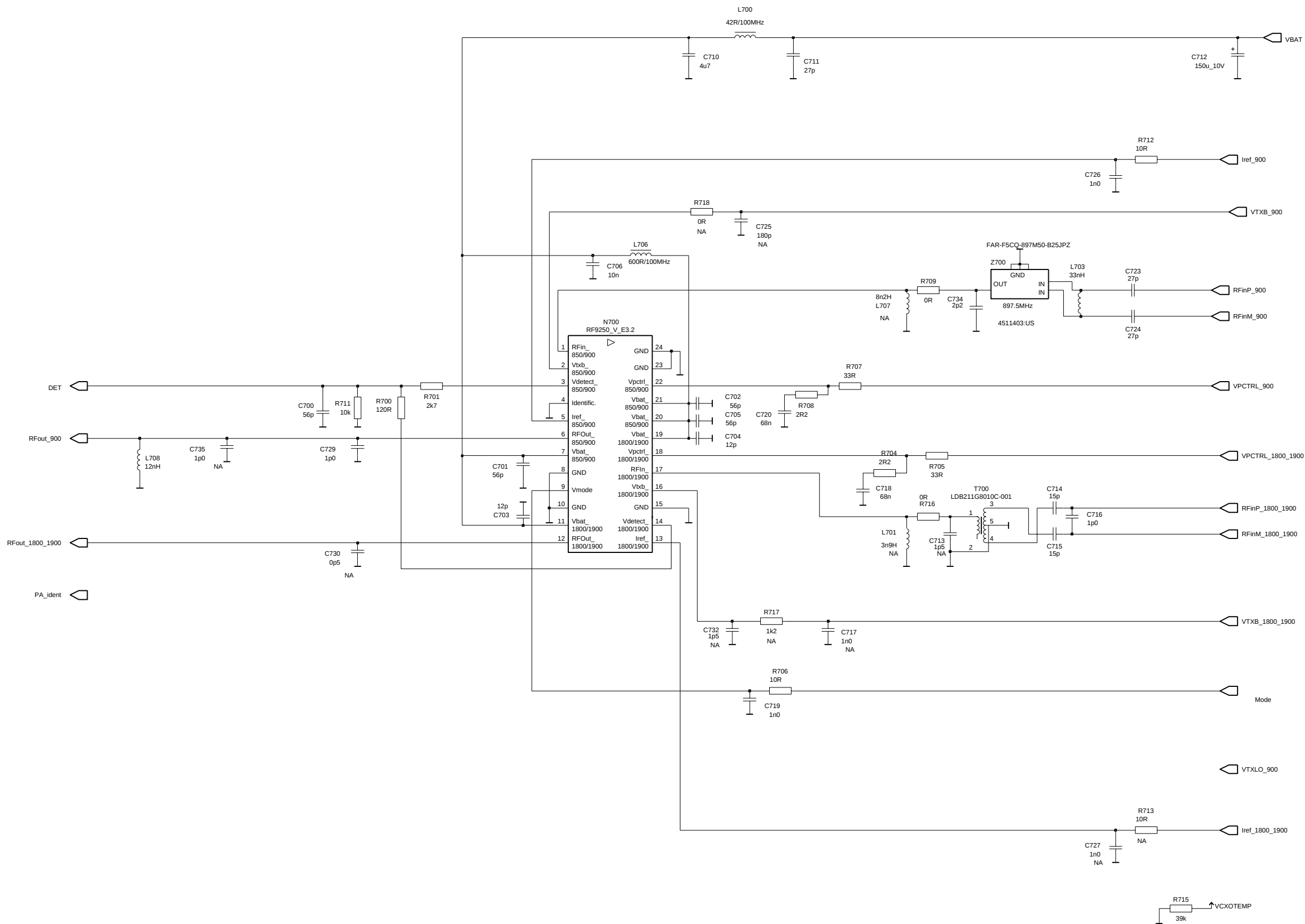
L511

T501

J509

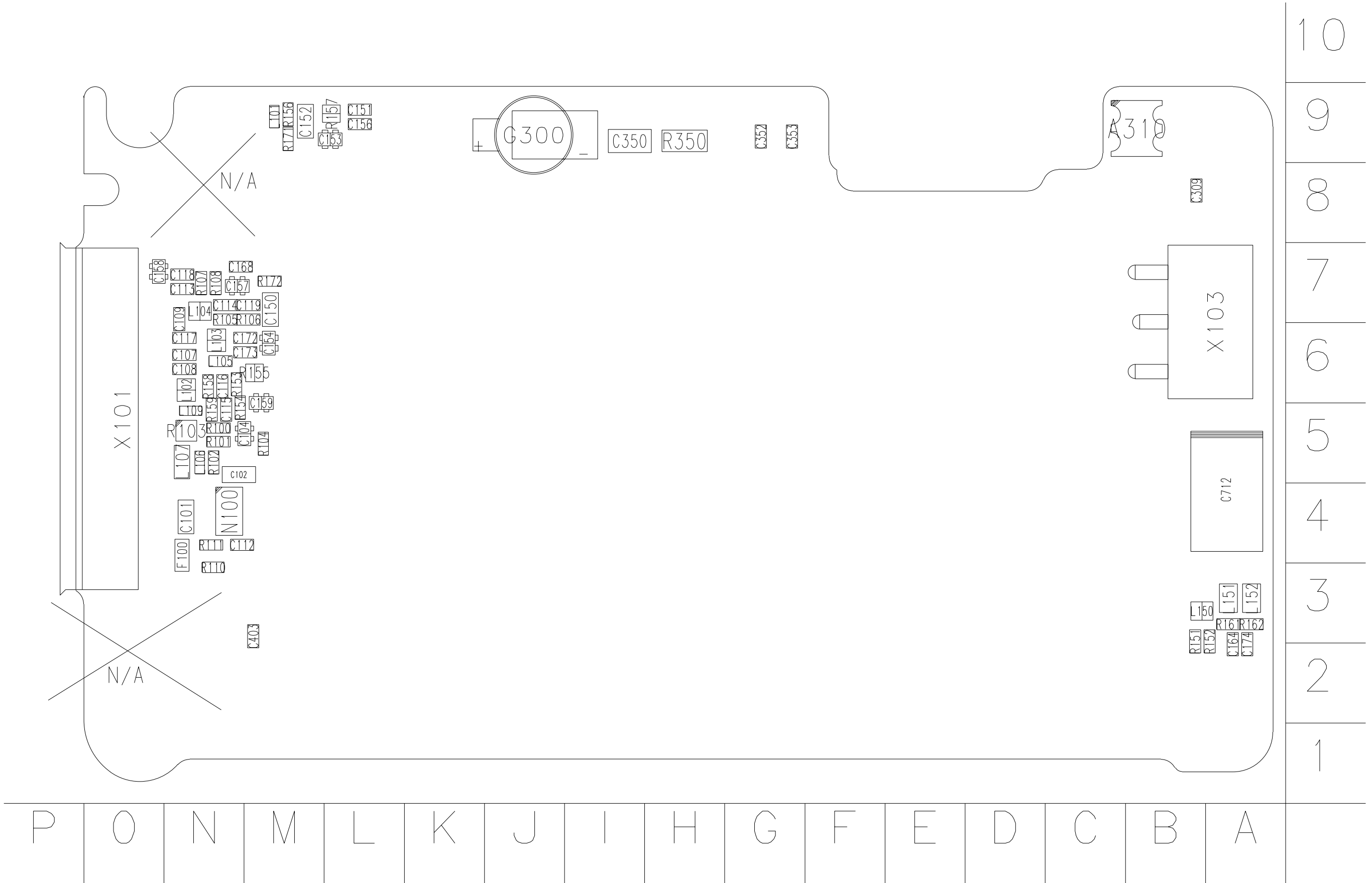
Tracking filter: R534, R535

TX



Last references:
C734
R718
L708

Component Placement Diagram Bottom



Component Placement Diagram Top

